

## FPGA Oriented HW/FW Development in FEA DESY

*Tuesday, December 4, 2012 2:05 PM (25 minutes)*

New FPGA Technologies (like XILINX Series 7 FPGAs) provide new and elegant approaches for high speed DAQ systems. High speed transceivers with data rates of more than 10Gbit/s and new scalable internal bus systems simplify these developments.

Our group is working together with KIT on the development of a versatile DAQ card in the  $\mu$ TCA / AMC standard.

This talk will give an overview about the current developments and an outlook to possible future activities.

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