

Development of a FPGA based DAQ for digital SiPM's using USB 3.0

Monday, December 3, 2012 2:50 PM (25 minutes)

The presentation will show concept and developments for a read-out system for Philips digital SiPM's. It utilizes a Spartan 6 FPGA to connect and to transfer data from these photon counting device. A USB 3.0 interface is used as a high-speed link of up to 5 Gbit/s to transfer the sensor data to a PC.

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