

Session Program

Dec 3 - 4, 2012

Topical Workshop: FPGAs in Research - Applications, Technologies and Tools

Talks MO2

Zentralinstitut für Elektronik, Forschungszentrum Jülich, Room 110 in Building 2.5

Mon, December 3

3:45 PM

Talks MO2

Session | **Location:** Zentralinstitut für Elektronik, Forschungszentrum Jülich, Room 110 in Building 2.5

15:45 – 16:10

The ATCA based Compute Node and its application in the Belle II PXD-DAQ

Speaker

Bjoern Spruck

16:10 – 16:35

The Juelich Digital Readout System for $\overline{\text{P}}$ anda development

Speaker

Simone Esch

16:35 – 17:00

Data Concentrator for the Belle II DEPFET Pixel Detector

Speaker

Michael Schnell

17:00 – 17:25

Firmware-Entwicklung für die ATLAS IBL BOC Karte

Speaker

Mr Marius Wensing

5:25 PM