Session Program

Dec 3 – 4, 2012

Topical Workshop: FPGAs in Research - Applications, Technologies and Tools

Invited Talk

Zentralinstitut für Elektronik, Forschungszentrum Jülich, Room 110 in Building 2.5

Tue, December 4

9:00 AM

Invited Talk

Session | **Location:** Zentralinstitut für Elektronik, Forschungszentrum Jülich, Room 110 in Building 2.5

09:00 – 10:30   **High Level Synthesis with Xilinx HLS**

Speaker
Mr Eugen Krassin

10:30 AM