

Session Program

Dec 3 - 4, 2012

Topical Workshop: FPGAs in Research - Applications, Technologies and Tools

Talks TUE2

Zentralinstitut für Elektronik, Forschungszentrum Jülich, Room 110 in Building 2.5

Tue, December 4

1:15 PM

Talks TUE2

Session | **Location:** Zentralinstitut für Elektronik, Forschungszentrum Jülich, Room 110 in Building 2.5

13:15 – 13:40

Field Programmable Gate Array Based Data Digitisation with Commercial Elements

Speaker

Mr Cahit Ugur

13:40 – 14:05

An FPGA Platform for Ultra-fast Data Acquisition

Speaker

Dr Michele Caselle

14:05 – 14:30

FPGA Oriented HW/FW Development in FEA DESY

Speaker

Mr Frantisek Krivan

14:30 – 14:55

FPGA- Framework development for a MicroTCA system

Speaker

Mrs Qingqing Xia

14:55 – 15:20

Dynamische Re -Konfiguration eines FPGAs mittels internem Controller

Speaker

Mr Markus Dick

3:20 PM