

Program MSE  
MSE Day 18.11.2022

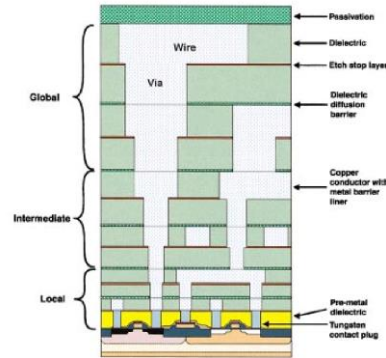
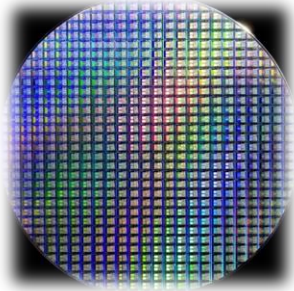
# Printed Electronic Devices and Circuits

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**RU Aghassi**



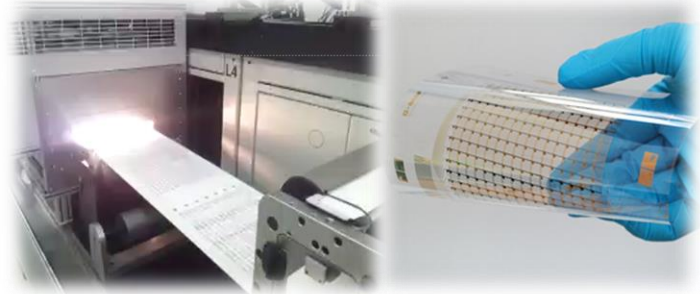
# Why printing electronics?

## Silicon Technology



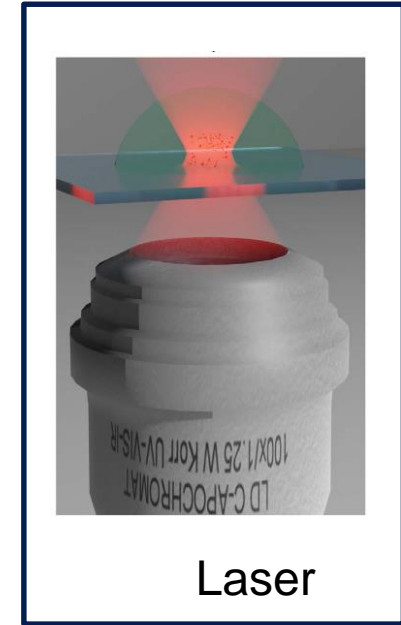
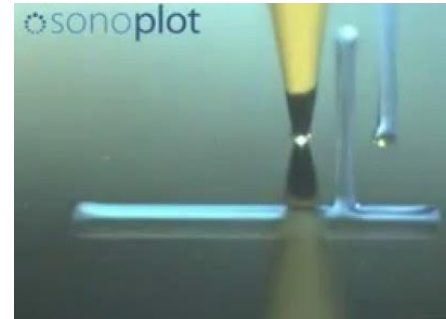
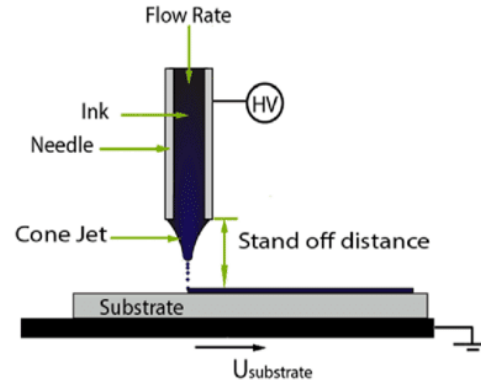
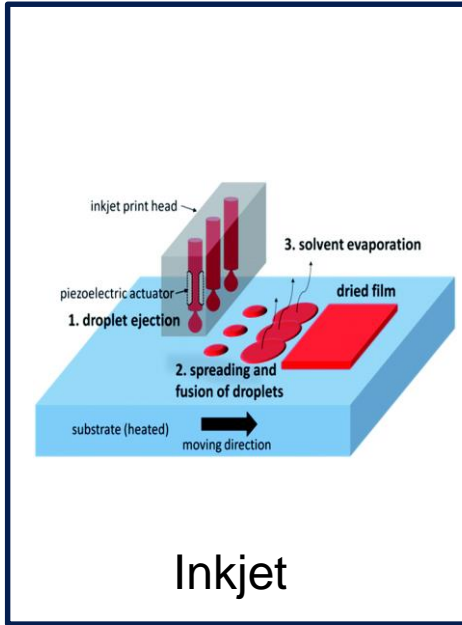
- nm-technology
- Performance up to ~GHz
- Subtractive processes needed
- Modern CMOS requires 37 masks and ~300 process steps

## Digital Printing Technology

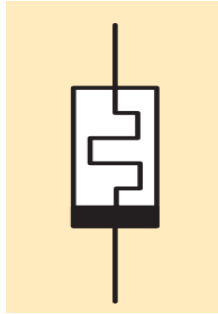


- $\mu\text{m}$ -technology
- Performance Hz-kHz, maybe MHz achievable
- Only needed materials are deposited
- Maskless, additive, large area, low cost

# Printing techniques



# Printed memristors

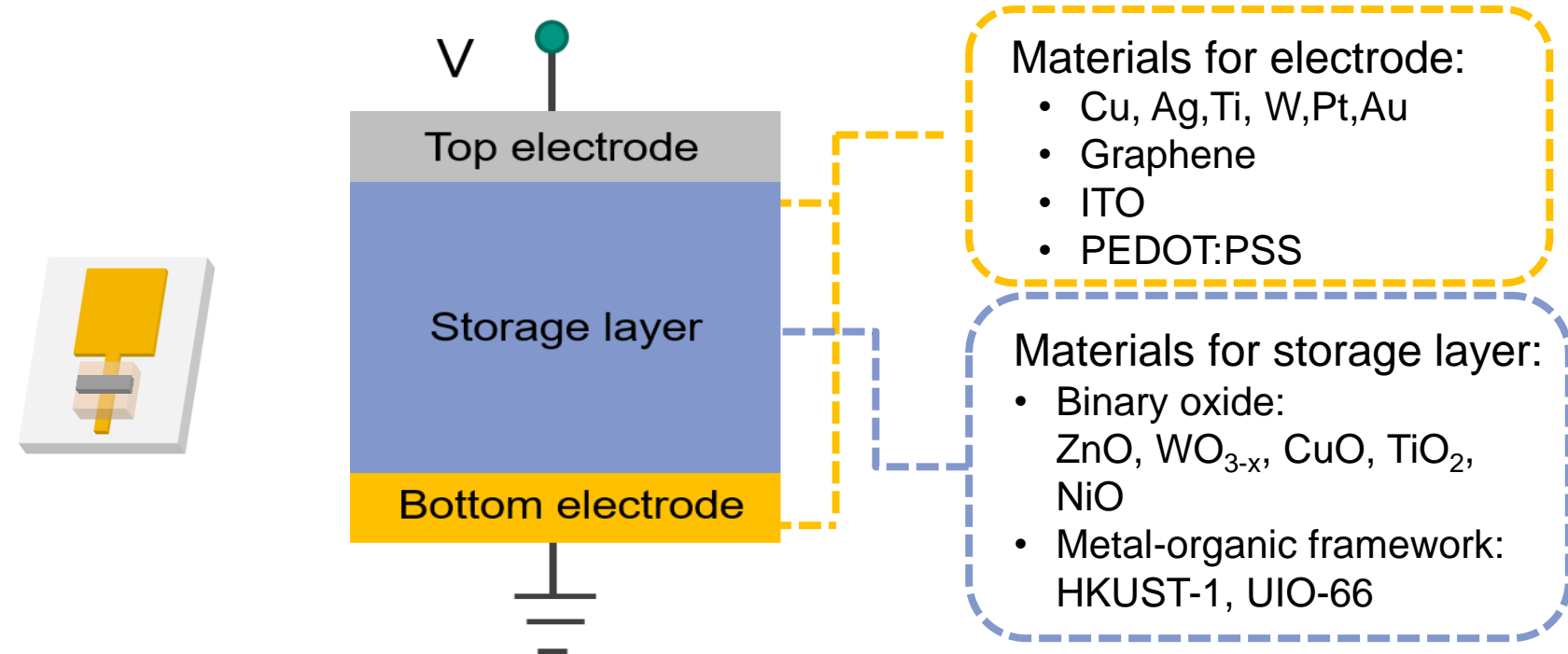


2 terminal  
electronic device  
realized in many  
nanoscale systems

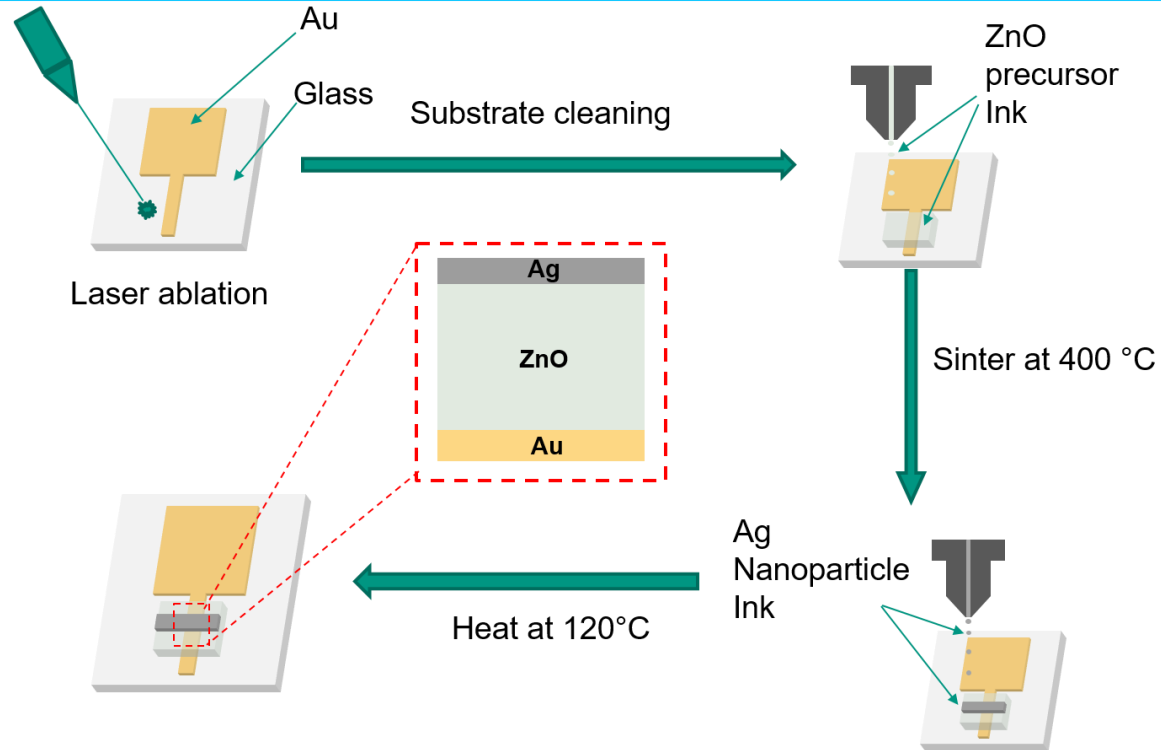
Memristor  
symbol

- Fundamental device (similar to L, C, R) postulated in 1971 by Leon Chua and found in 2008 (Strukelj et al., Nature 2008 and others)
- Physical concept relies on solid-state electronic and ionic transport coupled under an external bias voltage
- Resistance can be switched under an applied electrical field (direction & magnitude)
- Most promising application: non-volatile memory, neuromorphic computing, Hardware security

# Memristor structure

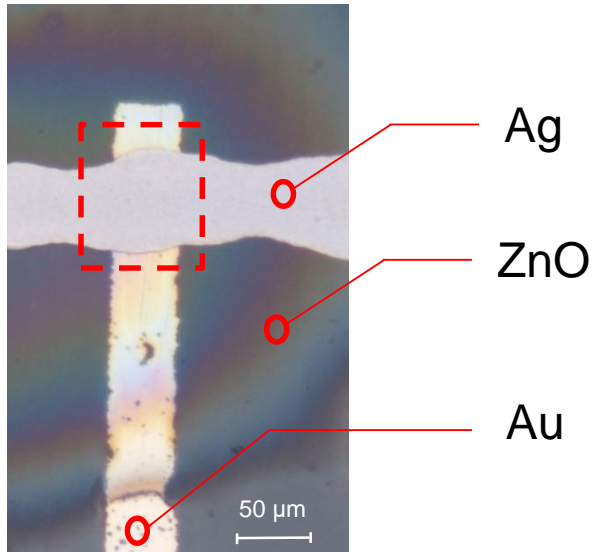


# Fabrication process of inkjet-printed memristor



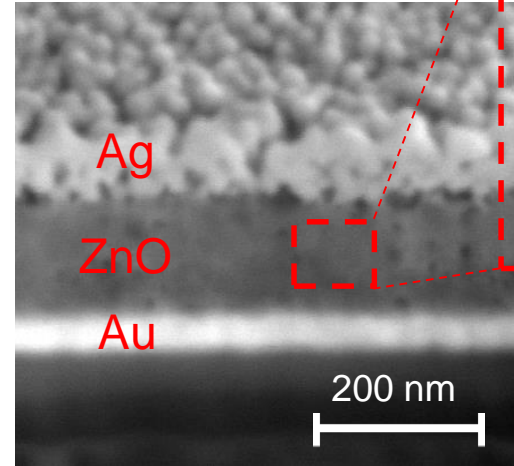
# Inkjet-printed memristor

Top view  
(optical photo)

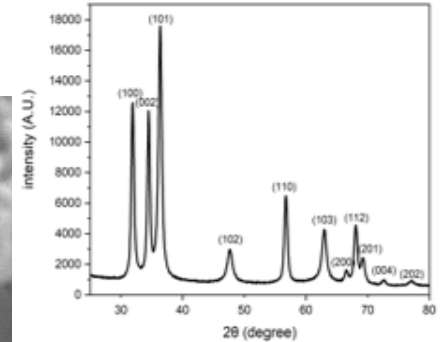


Width of electrodes:  $\sim 50 \mu\text{m}$

Cross-section  
(FIB-SEM)



XRD

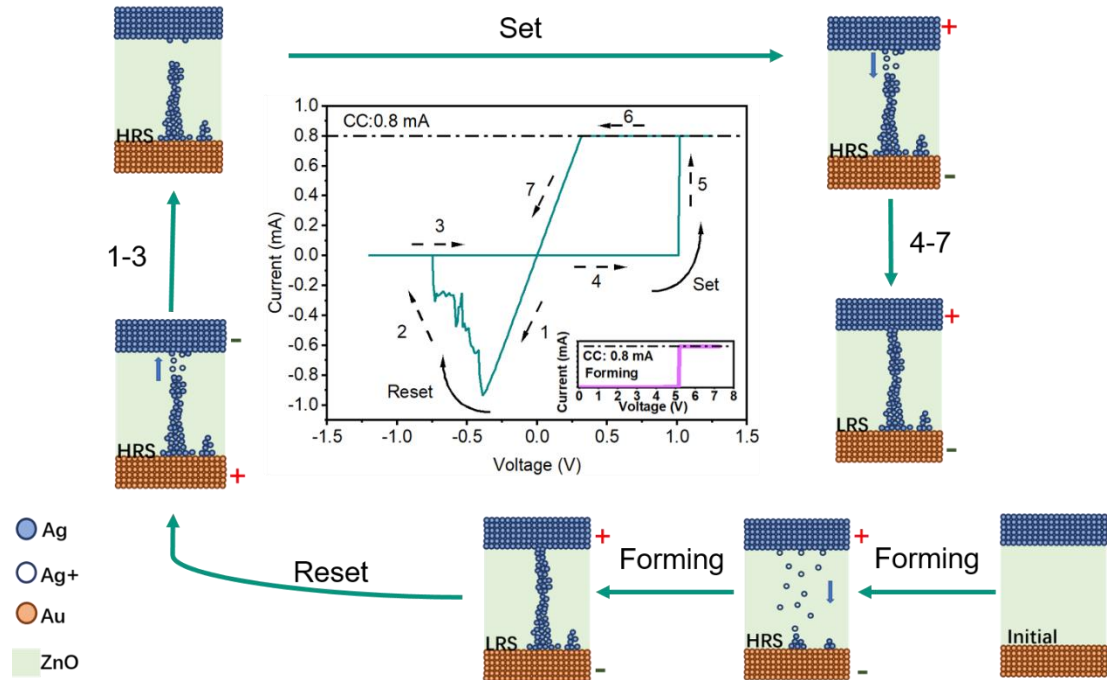
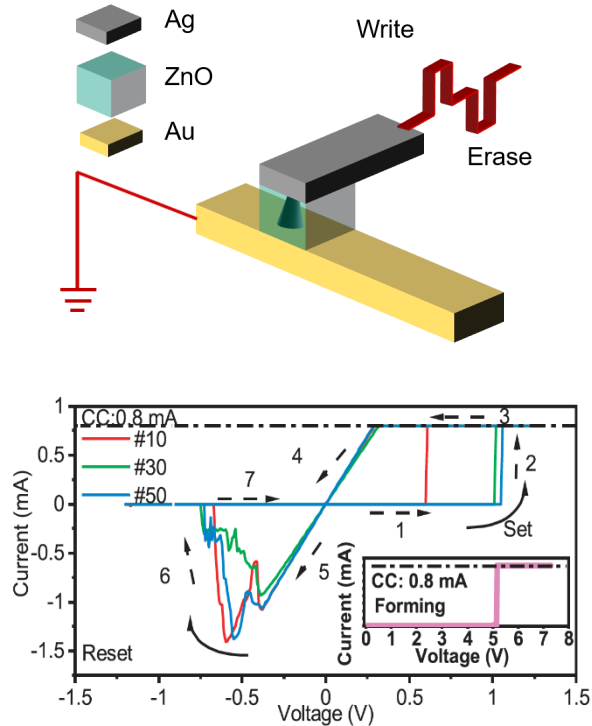


Thickness:

- Ag:  $\sim 100 \text{ nm}$
- ZnO:  $\sim 250 \text{ nm}$
- Au:  $50 \text{ nm}$

Hu, et al. Applied Physics Letters 119.11 (2021)

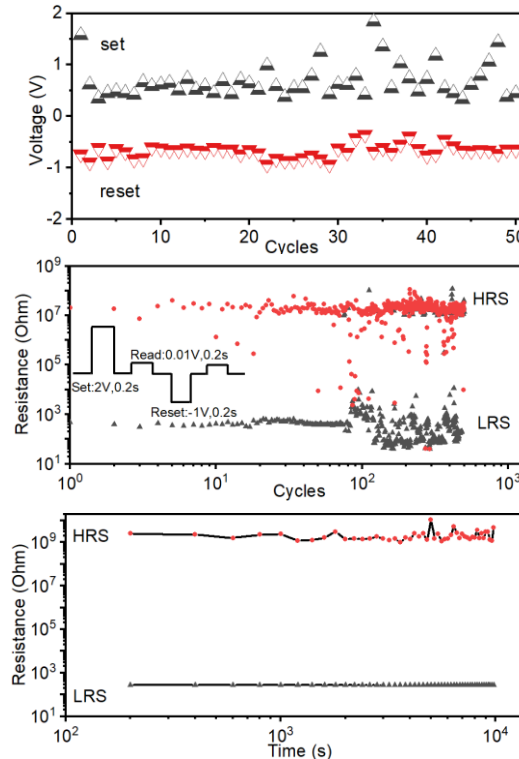
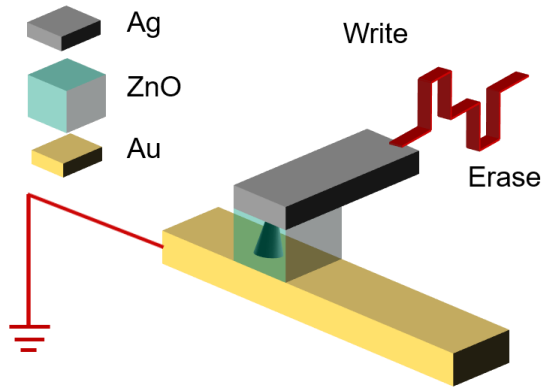
# Resistive switching mechanism



Hu, et al. Applied Physics Letters 119.11 (2021)



# Inkjet-printed memristor performance



High uniformity

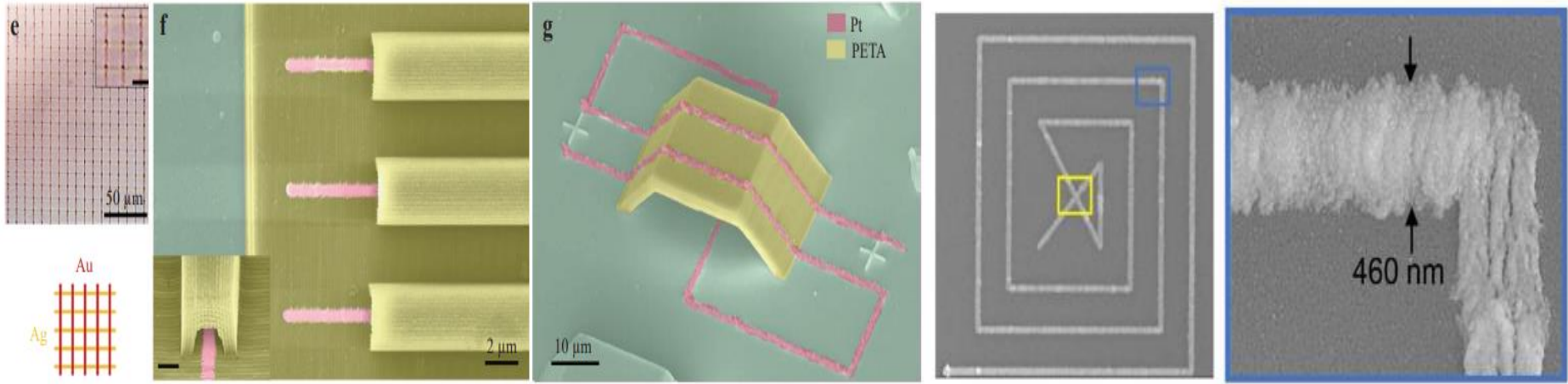
Endurance under dynamic pulse voltage mode over 500 cycles

Excellent retention performance over  $10^4$  s (non-volatile)

Hu, et al. Applied Physics Letters 119.11 (2021)

# Multi-photon multi-material laser printing

Polymers, metals, and semiconductors can be laser printed

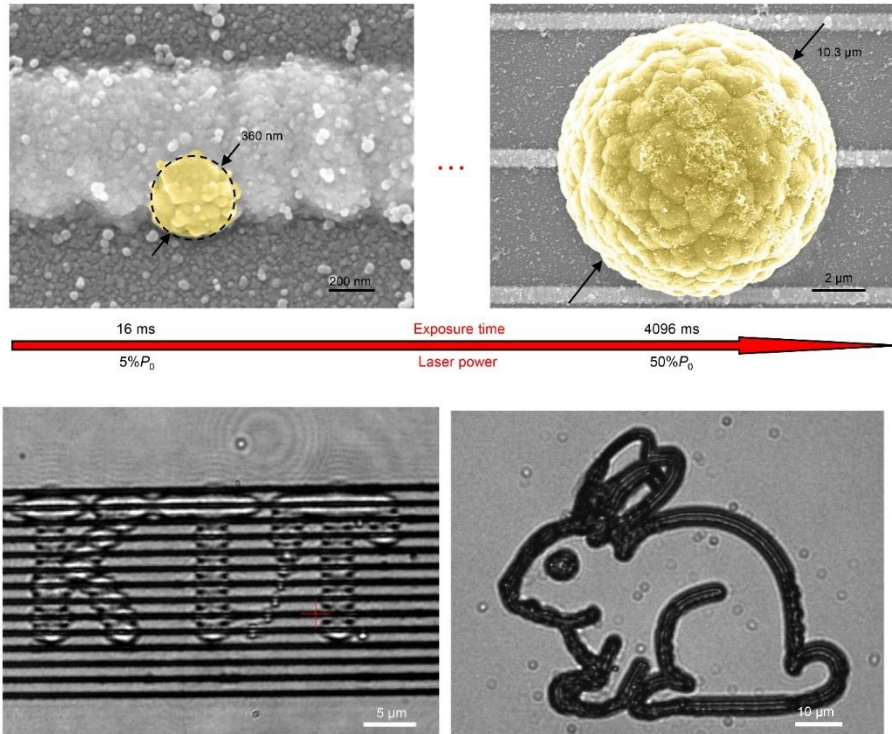


Yang, Liang, et al Light: Advanced Manufacturing 2.3 (2021)

Yang, Liang, et al. Laser & Photonics Reviews 16.3 (2022)

Work from AG Wegener, T2

# Laser-printed semiconductor: ZnO

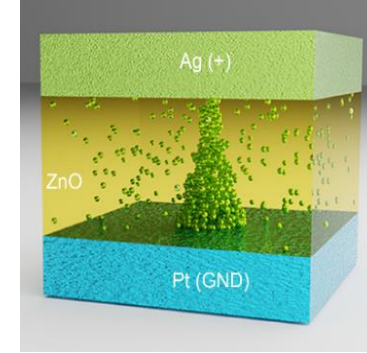
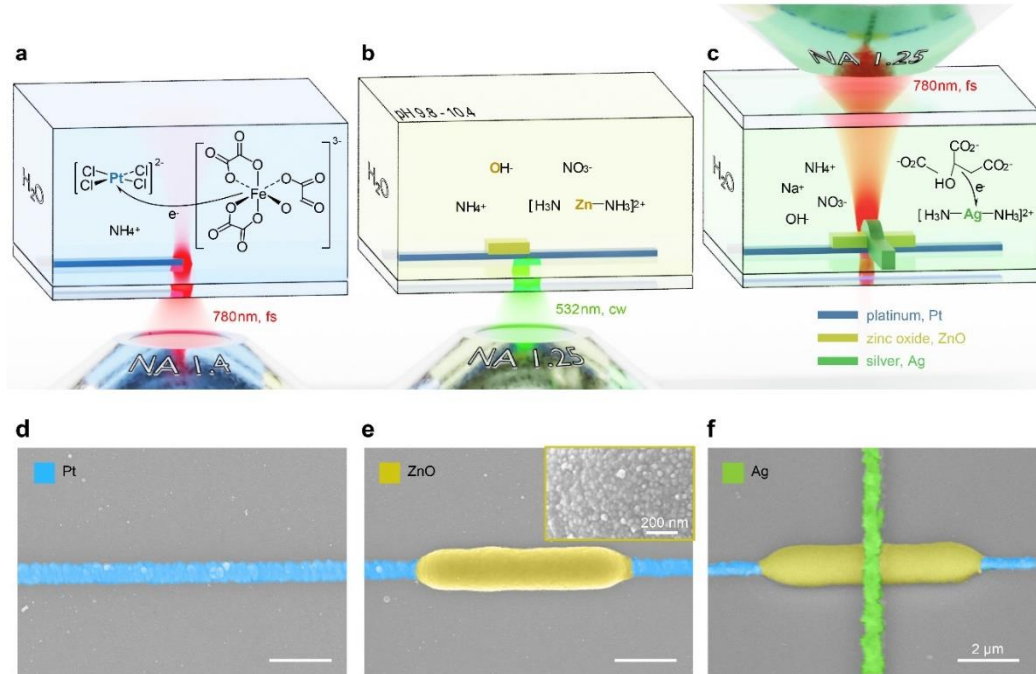


- Size of ZnO hemisphere can be controlled by laser power and exposure time
- ZnO can be fast laser printed in arbitrary form (speed of 100  $\mu\text{m/s}$ )

Joint work (AG Wegener, AG Aghassi, AG Blasco, and AG Barner-Kowollik)

Liang et. al. submitted

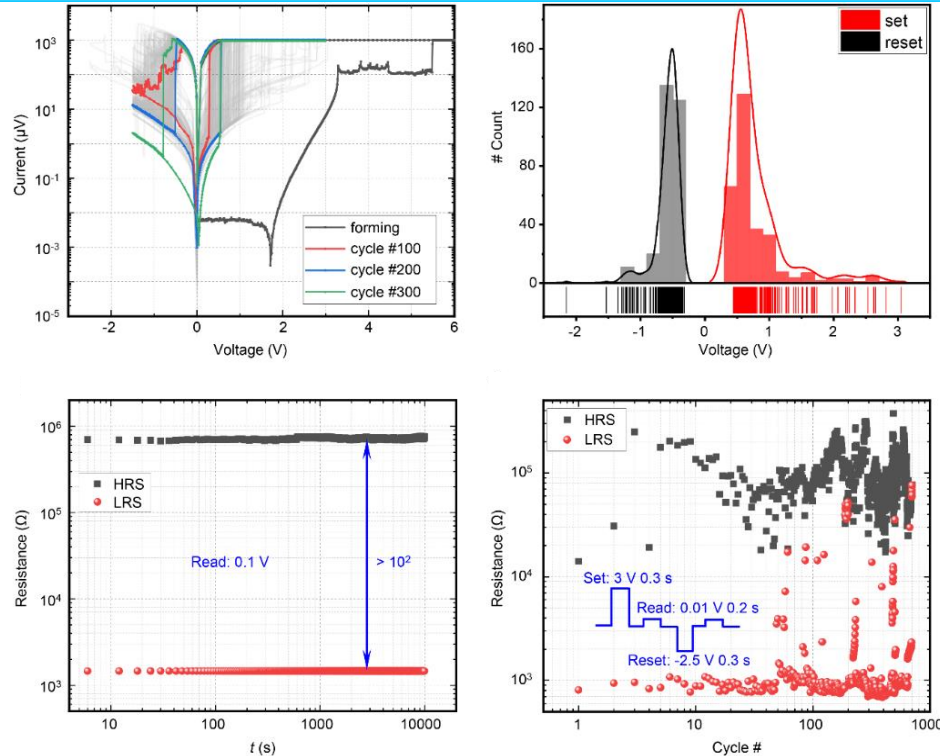
# Laser-printed Pt-ZnO-Ag memristors



- Novel recipe for ZnO
- laser is used to write and simultaneously sinter the semiconductor
- Feature size below 1  $\mu\text{m}$

Liang et. al. submitted

# Laser-printed memristor performance



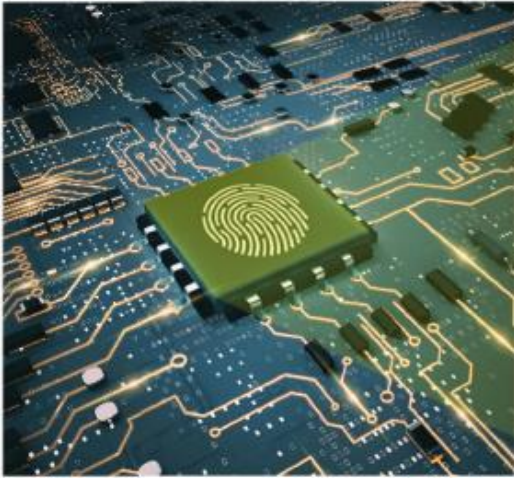
- Bipolar resistive switching
- Small voltage variability
- Retention over  $10^4$  s
- Endurance over 700 cycles

Liang et. al. submitted

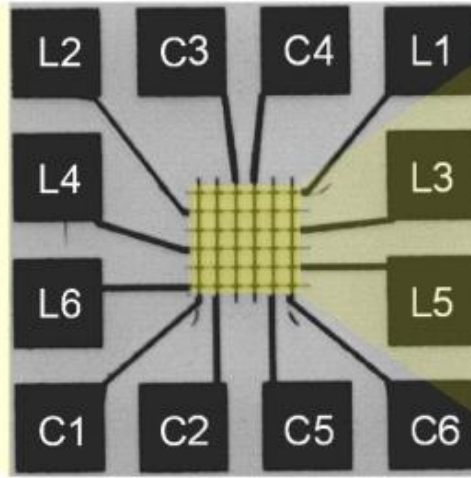


# Memristive crossbar array for security circuit

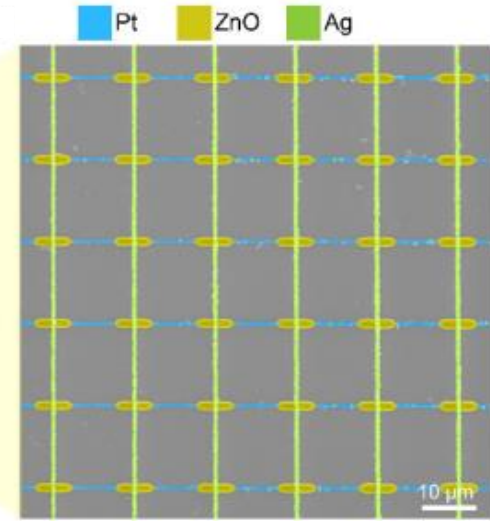
## Physically unclonable function (PUF)



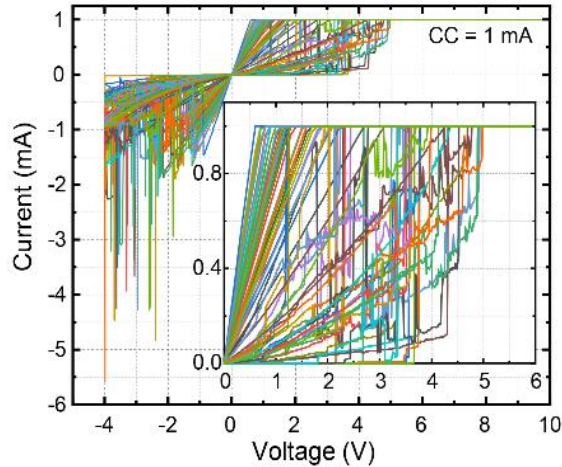
Hardware security



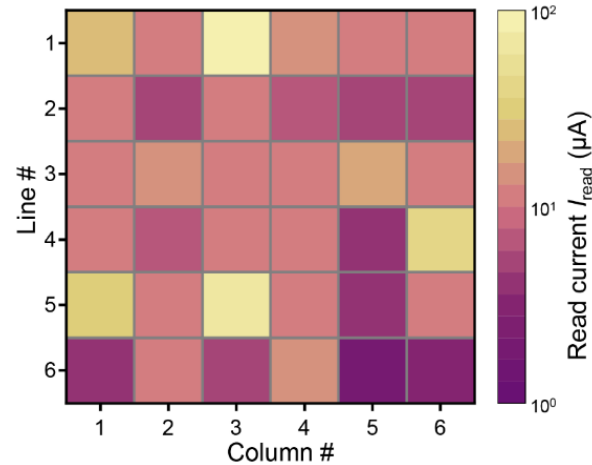
6 x 6 crossbar structure consisting of 36 memristors, fabricated within one setup



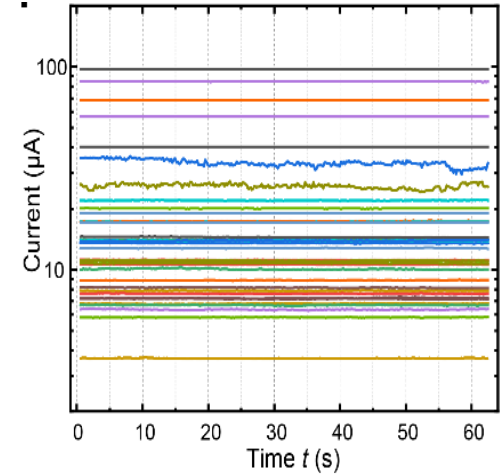
# Memristive crossbar arrays for security circuits



Activation of 36 memristors



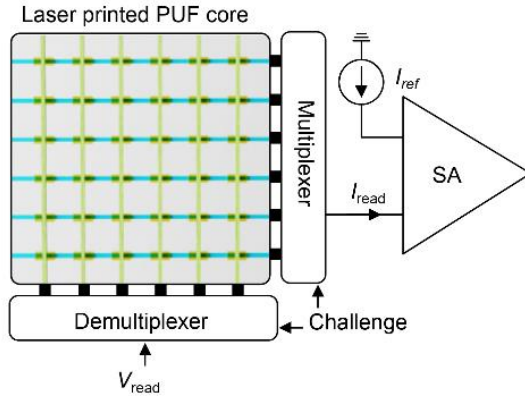
$I_{\text{read}}$  of 36 activated memristors



Low noise of  $I_{\text{read}}$

Liang et. al. submitted

# Security keys out of stochastic currents

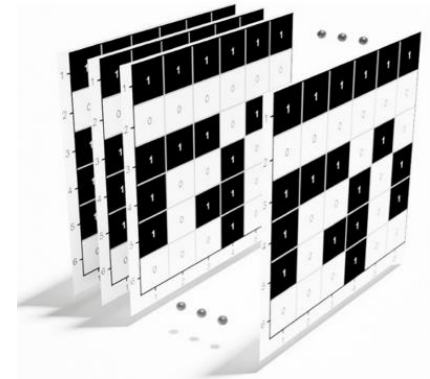


Circuit architecture  
around memristor-PUF

Diagram illustrating the bit-array distribution, showing a 6x6 grid of bits (0 or 1) corresponding to Line # and Column #.

Line #	1	2	3	4	5	6
1	1	1	1	1	1	1
2	0	0	0	0	0	0
3	1	1	1	0	1	0
4	1	0	0	1	0	1
5	1	0	1	1	0	1
6	0	0	0	1	0	0

Bit-array distribution

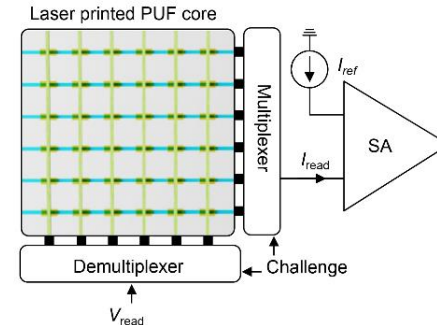
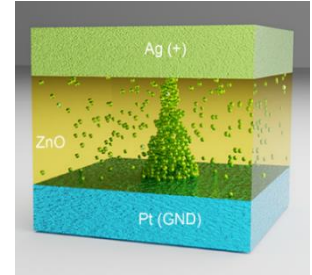
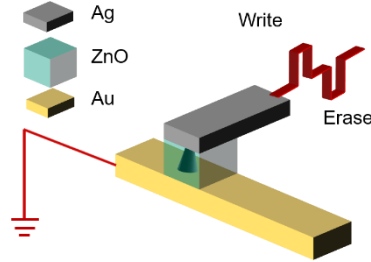


No bit errors over 300 cycles

Liang et. al. submitted

# Summary

- Inkjet-printed memristor
- Laser-printed memristor
- Laser-printed memristive crossbar array as hardware security circuit



# Acknowledgments

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