

Topical Workshop: DTS-Platform



Report of Contributions

Contribution ID: 0

Type: **not specified**

Imbiss und informeller Austausch

Monday, October 16, 2017 12:00 PM (1 hour)

Contribution ID: 1

Type: **not specified**

Status der DTS Plattform

Monday, October 16, 2017 1:00 PM (30 minutes)

Zusammenarbeit, Kommunikation, Projekte, Technik;
DTS-Cloud (Verwendung für Präsentationen und Kurzüberblick); Neues im GIT (FPGA + Mikro-
controller);
Nutzung für Quellen mit Repositories der Zentren

Presenter: KAEVER, Peter

Contribution ID: 2

Type: **not specified**

Vorschlag FPGA-Dokumentation Schnittstellen FPGA-Register & Diskussion

Monday, October 16, 2017 1:30 PM (1 hour)

Presenter: BORMANN, Dietmar

Contribution ID: 3

Type: **not specified**

TDC/Timestamp - Übersicht (HZG, HZDR, GSI, DESY)

Monday, October 16, 2017 2:30 PM (1 hour)

Contribution ID: 4

Type: **not specified**

Session: Betriebssysteme, slow control, Plattformen, Verfügbarkeit

Monday, October 16, 2017 4:00 PM (1 hour)

FreeRTOS; Linux; Interesse an Board support package für Zynq (Ultrascale+)

Contribution ID: 5

Type: **not specified**

Memory transfer from FPGA to GPU and real-time storage

Monday, October 16, 2017 5:00 PM (30 minutes)

Presenter: CHILINGARYAN, Suren

Contribution ID: 6

Type: **not specified**

GPU-Framework @KIT

Monday, October 16, 2017 5:30 PM (30 minutes)

Presenter: VOGELGESANG, Matthias (IPE)

Contribution ID: 7

Type: **not specified**

Diskussion

Monday, October 16, 2017 6:00 PM (1 hour)

Contribution ID: 8

Type: **not specified**

Session: Backend –Situation/Lösungen

Tuesday, October 17, 2017 9:30 AM (10 minutes)

Contribution ID: 9

Type: **not specified**

10 Gb/s Ethernet FPGA-Core (KIT, DESY, HZDR, ...)

Tuesday, October 17, 2017 9:40 AM (20 minutes)

Contribution ID: **10**

Type: **not specified**

10 Gb/s Konzentrator (FZJ)

Tuesday, October 17, 2017 10:00 AM (20 minutes)

Presenter: VÖLKEL, Sebastian

Contribution ID: 11

Type: **not specified**

Entwicklung von optischen Highspeed-Links für Detektoren (KIT)

Tuesday, October 17, 2017 10:20 AM (20 minutes)

Presenter: Dr SCHNEIDER, Marc (Karlsruher Institut für Technologie)

Contribution ID: 12

Type: **not specified**

Frontend: schnelle ADC / DAC mit JESD Interface; lizenzfrei (KIT, HZDR, ..)

Tuesday, October 17, 2017 10:40 AM (20 minutes)

Contribution ID: **13**

Type: **not specified**

Session: Hardware

Tuesday, October 17, 2017 11:30 AM (10 minutes)

Contribution ID: **14**

Type: **not specified**

MTCA Boards von HGF-Zentren

Tuesday, October 17, 2017 11:40 AM (40 minutes)

Contribution ID: 15

Type: **not specified**

New MTCA Generation „HGF-AMC2”

Tuesday, October 17, 2017 12:20 PM (40 minutes)