

Session Program

Sep 2 - 3, 2025

ECFA DRD 7.6b Workshop on Future Detector Technologies

Plenary session: Physics Requirements & Integration Technologies III

KIT Campus South, Kleiner Hörsaal Elektrotechnik (Geb. 11.10)
Engelbert-Arnold-Straße 5

Tue, September 2

4:20 PM

Plenary session: Physics Requirements & Integration Technologies III

Session | **Location:** KIT Campus South, Kleiner Hörsaal Elektrotechnik (Geb. 11.10), Engelbert-Arnold-Straße 5 |

Convener: Ladislav Andricek

4:20 – 4:50 PM

RISC-V and FPGA Integration on Detector

Speaker

Michael Karagounis

4:50 – 6:00 PM

Round Table Discussion: Open Questions and Priorities - PilotProject

Speaker

All Participants

6:50 PM